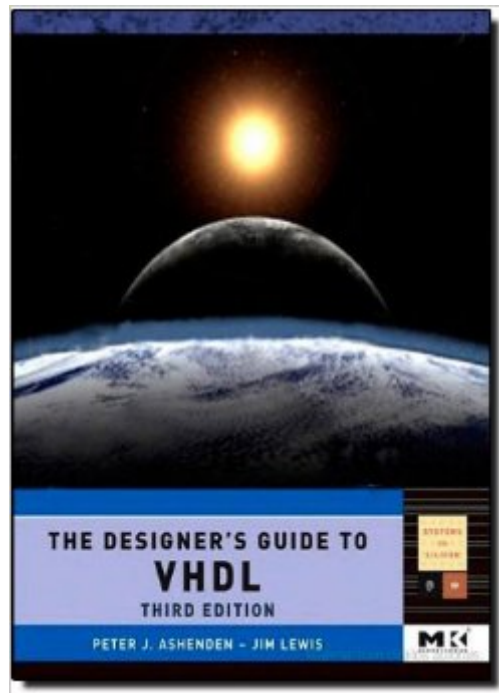


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# The Designer's Guide To VHDL, Third Edition (Systems On Silicon)



## Synopsis

VHDL, the IEEE standard hardware description language for describing digital electronic systems, has recently been revised. This book has become a standard in the industry for learning the features of VHDL and using it to verify hardware designs. This third edition is the first comprehensive book on the market to address the new features of VHDL-2008. \* First comprehensive book on VHDL to incorporate all new features of VHDL-2008, the latest release of the VHDL standard...helps readers get up to speed quickly with new features of the new standard.\* Presents a structured guide to the modeling facilities offered by VHDL...shows how VHDL functions to help design digital systems.\* Includes extensive case studies and source code used to develop testbenches and case study examples..helps readers gain maximum facility with VHDL for design of digital systems.

## Book Information

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## Customer Reviews

I'm sure this book has its intended audience and uses, but I have some complaints specific to my purpose. I need to learn VHDL to create some hardware within the next 2-3 months. First, it takes Ashenden about 1000 words to say something that can be easily and concisely explained in 100 words. Secondly, his style is very formal and abstract, and makes little effort to relate VHDL to the actual hardware/physical implementation. For an HDL, I feel putting the code in context with the hardware is very important. Finally, and most importantly, I find his method of explaining things

frustrating. He starts with a very abstract, formal syntactical definition, and then gives a specific example. OK, that's all fine. But he doesn't fill in the middle. What about the details and specifics? They might be there, but if so, they are probably buried in pages of discussion. He teaches VHDL like you might teach English grammar. The problem is, I already know assembly, C/C++, Matlab, Java and digital logic, so it would speed learning if he would relate or define the concepts in terms that most electrical engineers have general competency in. Instead, he leaves me scratching my head for 15 minutes trying to figure out what he means by something as elementary as a multi-dimensional array. I'm sure this book is a great Bible for people setting out to make a career in VHDL development--those who want the "pure" and the "true" religion. But for the practicing researcher or scientist who just wants to make some relatively simple device, it should serve more as a reference text than a learning guide. But even as a reference text it falls short, so I'm left wondering what it is particularly useful for. The review by Emmett Chadwick Bearden is spot on!

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